



Dkt. 2271/56389-Z

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Application of: Kazuhiko ADACHI

Serial No.: 10/626,170

Group Art Unit: 2814

Date Filed: July 23, 2003

Examiner: Douglas A. Wille

For: SEMICONDUCTOR DEVICE HAVING A FLIP-CHIP CONSTRUCTION

Conf. No.: 9714

Class-Subclass: 438-108000

Issue Fee Due Date: March 22, 2005

Mail Stop Issue Fee

Commissioner for Patents

P.O. Box 1450

Alexandria, VA 22313-1450

**APPLICANT'S COMMENTS ON
EXAMINER'S STATEMENT OF REASONS FOR ALLOWANCE**

Sir:

Applicant appreciates the Examiner's Statement of the Reasons for Allowance attached to the Notice of Allowability dated December 22, 2004 and submits that the allowed claims recite subject matter which further supports patentability for reasons in addition to those identified in the Reasons for Allowance.

Respectfully submitted,

PAUL TENG, Reg. No. 40,837
Attorney for Applicant
Cooper & Dunham LLP
1185 Avenue of the Americas
New York, N.Y. 10036
Tel.: (212) 278-0400

I hereby certify that this correspondence is being deposited this date with the U.S. Postal Service with sufficient postage as first class mail in an envelope addressed to: Mail Stop Issue Fee, Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.

Paul Teng
Reg. No. 40,837

March 8, 2005
Date